

ACCESS NAMES TABLE

SOURCE ACCESS NAME= PPC2.P359.SRC.MVDN
OBJECT ACCESS NAME= PPC2.P359.OBJ.MVDNS
LISTING ACCESS NAME= PPC2.P359.LST.MVDNS
ERROR ACCESS NAME=
OPTIONS= XREF
MACRO LIBRARY PATHNAME=

LINE	KEY	NAME
0002	A	VERSION =>PPC2.P359.SRC.P359

0031
0032
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0050

IDT 'MVDN'

```
*****  
*  
*      MM      MM      V      V      DDDDDDDD      N      N      *  
*      M M    M M      V      V      D      D      NN      N      *  
*      M M    M      V      V      D      D      N N      N      *  
*      M      M      V      V      D      D      N N      N      *  
*      M      M      V      V      D      D      N N      N      *  
*      M      M      V V      D      D      N      NN      *  
*      M      M      V      DDDDDDDD      N      N      *  
*  
*      PPPP      3333      555555      9999      *  
*      P P      3 3      5      9 9      *  
*      P P      3 3      5      9 9      *  
*      PPPP      3333      55555      99999      *  
*      P      3 3      5 5      9 9      *  
*      P      3 3      5 5      9 9      *  
*      P      3333      5555      9999      *  
*  
*****
```

```

0055 * WITHOUT ERAM : Move the contents in VDP RAM from
0056 * a lower address to a higher address
0057 * avoiding a possible over-write of data
0058 * ARG : byte count
0059 * VARO : source address
0060 * VARY2 : destination address
0061 *
0062 * WITH ERAM : Same as above except moves ERAM to ERAM

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0063 0000      DEF  MVDN, MVDN2
0064
0065 0000      REF  RAMTOP, VRAM, GRAM, ARG, VARY2, VARO
0066          REF  R3LB, R5LB, R12LB
0067          REF  VDPWD, VDPRD, WRVDP
0068
0069 0000
0070 0000 C060 MVDN  MOV  @ARG, R1          Get byte count
0071 0002 0000
0071 0004 C160      MOV  @VARY2, R5        Get destination
0072 0006 0000
0072 0008 C0E0      MOV  @VARO, R3          Get source
0073 000A 0000
0073 000C C1E0 MVDN2 MOV  @RAMTOP, R7       ERAM or VDP?
0074 000E 0000
0074 0010 1612      JNE  MV01          ERAM - so handle it
0075 0012 1002      JMP  MV05          VDP - so jump into loop
0076 0014 0605 MVDN1 DEC  R5
0077 0016 0603      DEC  R3
0078 0018      MV05
0079
*-----CONDITIONAL ASSEMBLY-----*
0080      ASMIF VERS=DX10
0081      MOV  R3, R14          Read address
0082      AI   R14, VRAM
0083      MOVB *R14+, R12       Read a byte
0084      MOV  R5, R14
0085      AI   R14, VRAM       Write address
0086      MOVB R12, *R14+
0087
0088      ASMELS
0089 0018
0090 0018 D7E0      MOVB @R3LB, *R15        Write out read address
0091 001A 0000
0091 001C D7C3      MOVB R3, *R15
0092 001E D1E0      MOVB @VDPRD, R7        Read a byte
0093 0020 0000
0093 0022 D7E0      MOVB @R5LB, *R15        Write out write address
0094 0024 0000
0094 0026 0265      ORI  R5, WRVDP        Enable VDP write
0095 0028 0000
0095 002A D7C5      MOVB R5, *R15
0096 002C D807      MOVB R7, @VDPWD       Write the byte
0097 002E 0000
0097
0098      ASMEND
*-----END OF CONDITIONAL ASSEMBLY-----*
0099 0030 0601      DEC  R1          One less byte to move
0100 0032 16F0      JNE  MVDN1       Loop if more to move
0101 0034 045B      RT

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```
0103 0036      MVO1
0104           *-----CONDITIONAL ASSEMBLY-----
0105           ASMIF VERS=DX10
0106           AI   R5,GRAM      Read address
0107           AI   R3,GRAM      Write
0108           ASMEND
0109           *-----END OF CONDITIONAL ASSEMBLY-----*
0110 0036 D553 MVDN#1 MOVB *R3,*R5      Move a byte
0111 0038 0603      DEC R3              Decrement destination
0112 003A 0605      DEC R5              Decrement source
0113 003C 0601      DEC R1              One less byte to move
0114 003E 16FB      JNE MVDN#1         Loop if more to move
0115 0040 045B      RT
0116           END
NO ERRORS,      NO WARNINGS
```

LABEL VALUE DEFN REFERENCES

ARG	R	0002'	0066	0070					
DX10		0001	0003	0004	0080	0105			
GRAM	R		0066						
MV01		0036'	0103	0074					
MV05		0018'	0078	0075					
MVNDN	D	0000'	0070	0064					
MVNDN#1		0036'	0110	0114					
MVNDN1		0014'	0076	0100					
MVNDN2	D	0000'	0073	0064					
P359		0000	0003	0003					
R1		0001		0070	0099	0113			
R12LB	R		0067						
R15		000F		0090	0091	0093	0095		
R3		0003		0072	0077	0091	0110	0111	
R3LB	R	001A'	0067	0090					
R5		0005		0071	0076	0094	0095	0110	0112
R5LB	R	0024'	0067	0093					
R7		0007		0073	0092	0096			
RAMTOP	R	000E'	0066	0073					
VAR0	R	000A'	0066	0072					
VARY2	R	0006'	0066	0071					
VDPRD	R	0020'	0068	0092					
VDPWD	R	002E'	0068	0096					
VERMAC	M		A0001	0003					
VERS		0000	0003	0004	0080	0105			
VRAM	R		0066						
WRVDP	R	0028'	0068	0094					