

ACCESS NAMES TABLE

SOURCE ACCESS NAME= PPC2.P359.SRC.GWRITE  
OBJECT ACCESS NAME= PPC2.P359.OBJ.GWRITES  
LISTING ACCESS NAME= PPC2.P359.LST.GWRITES  
ERROR ACCESS NAME=  
OPTIONS= XREF  
MACRO LIBRARY PATHNAME=

LINE	KEY	NAME
0002	A	VERSION =>PPC2.P359.SRC.P359

0031  
0032  
0033  
0034  
0035  
0036  
0037  
0038  
0039  
0040  
0041  
0042  
0043  
0044  
0045  
0046  
0047  
0048  
0049  
0050

IDT 'EWRITE'

```
*****  
*  
*      EEEEE  W   W   RRRR   IIIII  TTTTT  EEEEE  
*      E      W   W   R   R     I      T      E  
*      E      W   W   R   R     I      T      E  
*      EEEE   W   W   RRRR   I      T      EEE  
*      E      W WW W   R   R     I      T      E  
*      E      WW  WW  R   R     I      T      E  
*      EEEEE  W   W   R   R     IIIII  T      EEEEE  
*  
*      PPPP      3333      555555      9999  
*      P   P      3   3      5          9   9  
*      P   P      3   3      5          9   9  
*      PPPP      3333      55555      99999  
*      P          3   3      5   5      9   9  
*      P          3   3      5   5      9   9  
*      P          3333      5555      9999  
*  
*****
```

```

0055          DEF  GWRITE, GWITE1
0056 0000
0057          REF  VRAM, GRAM, ADDR1, ADDR11, ADDR2, BCNT1, PAD
0058          REF  GDST, CSRC, BCNT2
0059 0000
0060 0000
0061          *   Write the data which is stored in CPU to ERAM
0062          *   @GDST : Destination address on ERAM where data is going
0063          *           to be stored
0064          *   @CSRC : Source address on CPU  where data stored
0065          *   @BCNT2: byte count
0066 0000 0203  GWITE1 LI   R3, BCNT2           Count
0067          0002 0000
0067 0004 0202          LI   R2, GDST           Destination
0068          0006 0000
0068 0008 0201          LI   R1, CSRC          Source
0069          000A 0000
0069 000C 1006          JMP  GW$1
0070 000E
0071          *   Write the data which is stored in CPU to ERAM
0072          *   @ADDR1: Destination address on ERAM where data is going
0073          *           to be stored
0074          *   @ADDR2: Source address on CPU  where data stored
0075          *   @BCNT1: byte count
0076 000E 0203  GWRITE LI   R3, BCNT1           Count
0077          0010 0000
0077 0012 0202          LI   R2, ADDR1         Destination
0078          0014 0000
0078 0016 0201          LI   R1, ADDR2         Source
0079          0018 0000
0079 001A
0080          *           Common routine to copy from CPU to ERAM
0081          001A' GW$1  EQU   $
0082 001A C112          MOV   *R2, R4           Get destination address
0083          *-----CONDITIONAL ASSEMBLY-----*
0084          ASMIF VERS=DX10
0085          AI   R4, GRAM           Add in 990 ERAM offset
0086          ASMEND
0087          *-----END OF CONDITIONAL ASSEMBLY-----*
0088 001C C051          MOV   *R1, R1           Get CPU RAM address
0089 001E 0221          AI   R1, PAD           Add in CPU offset
0090          0020 0000
0090 0022 DD31  GW$2  MOVB  *R1+, *R4+         Move a byte
0091 0024 0613          DEC   *R3           Decrement the counter
0092 0026 16FD          JNE  GW$2           Loop if more to move
0093 0028 045B          RT           Return when finished
0094          END

```

NO ERRORS, NO WARNINGS

EWRITE LABEL	VALUE	DEFN	REFERENCES
\$	002A'		0081
ADDR1 R	0014'	0057	0077
ADDR11 R		0057	
ADDR2 R	0018'	0057	0078
BCNT1 R	0010'	0057	0076
BCNT2 R	0002'	0058	0066
CSRC R	000A'	0058	0068
DX10	0001	0003	0004 0084
GDST R	0006'	0058	0067
GRAM R		0057	
GW#1	001A'	0081	0069
GW#2	0022'	0090	0092
GWITE1 D	0000'	0066	0055
GWRITE D	000E'	0076	0055
P359	0000	0003	0003
PAD R	0020'	0057	0089
R1	0001		0068 0078 0088 0088 0089 0090
R2	0002		0067 0077 0082
R3	0003		0066 0076 0091
R4	0004		0082 0090
VERMAC M		A0001	0003
VERS	0000	0003	0004 0084
VRAM R		0057	